

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 249/236 DIV

In re the Application of

Bong-seok KIM

Art Unit:

Serial No.

Examiner:

*[Division of Serial No. 09/850,143, filed
May 8, 2001]*

Filed: July 22, 2003

*Confirmation
No.*

For: MERGED MEMORY AND LOGIC
SEMICONDUCTOR DEVICE OF SALICIDED
DUAL GATE STRUCTURE INCLUDING
EMBEDDED MEMORY OF SELF-ALIGNED
CONTACT STRUCTURE AND
MANUFACTURING METHOD THEREOF

PRELIMINARY AMENDMENT

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Prior to calculation of the statutory filing fee, kindly amend the application identified above as follows:

IN THE CLAIMS

Cancel Claims 1-12 without prejudice or disclaimer.

REMARKS

The claims of parent application Serial No. 09/850,143 were subject to a requirement for restriction as set forth in the Action mailed July 9, 2002. In response to that requirement, Applicants elected Claims 1-12 for prosecution in said parent application. Non-elected Claims 13 - 23 are presented for examination in this divisional application.

The accompanying text for the specification and claims has been presented in accordance with the guidelines set forth in the notice entitled "Pre-Grant Publication